1) Power supply

5V power supply. The HY-201 development board is powered by the USB to DC‐0005 power cable, which can be connected to the USB interface of the computer or the USB interface of the mobile phone charger. It is very convenient to use.

2) Download Logic Program

After the power supply of the development board is normal, connect the USB Blaster downloader, open the Quartus II software, and download the compiled pof file to the CPLD through the JTAG.

3) Validation

After downloading, test the output of the corresponding I/O port of the CPLD chip. The TTL high level should be about 3.3V, and the low level should be 0V.